

IN THE CLAIMS

Presented below are the amended claims in a clean, unmarked format under §1.121(c)(1).

19. (Twice Amended) A memory device, comprising memory storage and three different interfaces to operate the memory device in one of three different modes, wherein the memory storage and the three different interfaces reside in a common packaged component.

26. (Twice Amended) A component board, comprising:
a processor; and
a memory device coupled with the processor, the memory device comprising memory storage and three different interfaces to operate the memory device in one of three different modes, where in the memory storage and the three different interfaces reside in a common packaged component.

32. (Twice Amended) A computer system, comprising:
a peripheral device; and
a system board coupled to the peripheral device, the system board comprising:
a processor; and